

# Hong Zhao

## *RESEARCH ASSOCIATE ABOUT*

- Fairleigh Dickinson University, Professor of EE and CE
- Ph.D. 2004, New Jersey Institute of Technology
- Dates of Fellowship: 2/2-8/26, STFP fellowship program
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## *SUMMER RESEARCH DESCRIPTION*

- Providing Hardware-based Cyber Resilience for FPGA System
- Motivation: Cyber security has been pushed to the hardware platform to keep invaders out. With FPGA being widely used as hardware platform in more critical systems including AI applications, providing security and root of trust for the software running about it becomes a necessity.
- Objective: Focus on cost effective solution to the security challenge in FPGA bitstream by applying hardware security primitive and providing cyber resilience at platform level with emphasis on preventing stage.

### *Research interests:*

- *Hardware Security, Hardware Trojan Detection, FPGA bitstream protection*
- *Network Security, Physical layer communication security*
- *Embedded System Security, FPGA based IoT Security.*